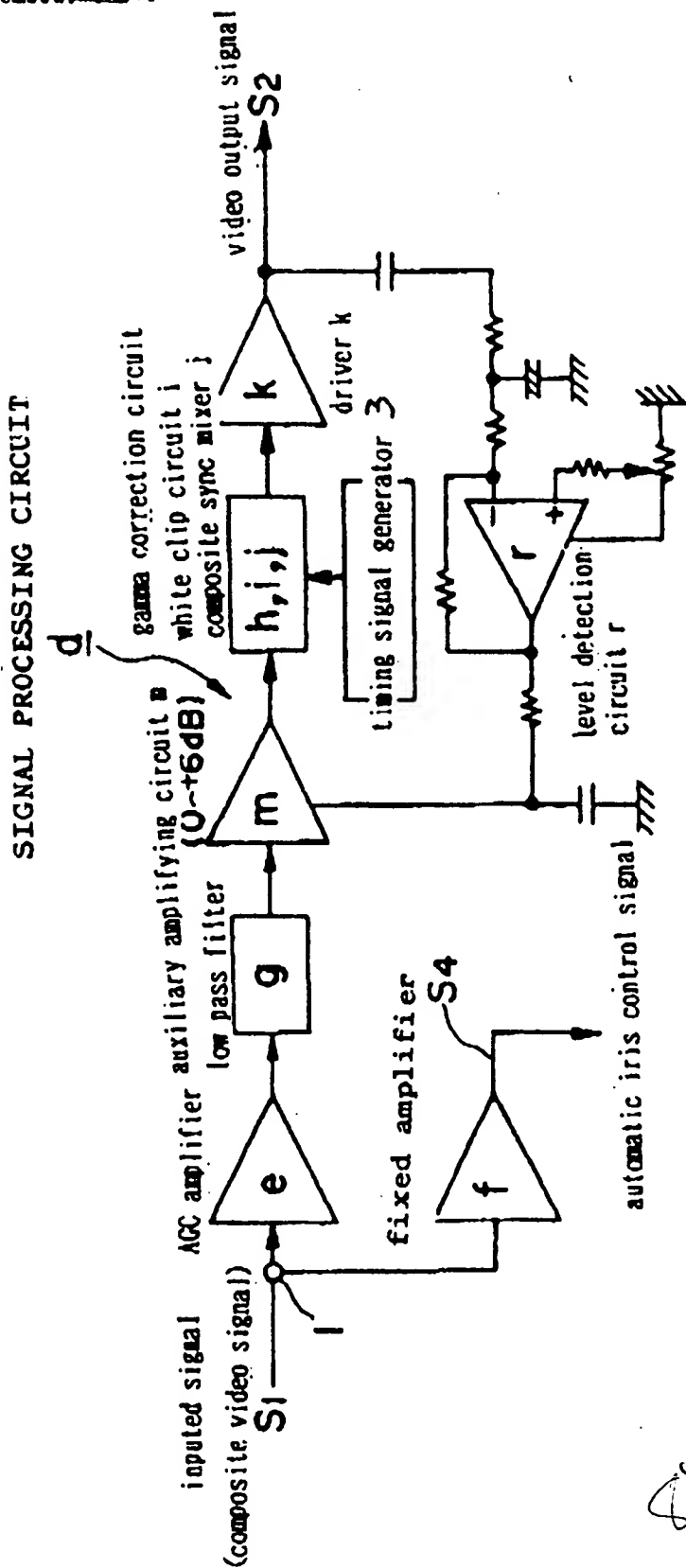


Fig 1 embodiment 1



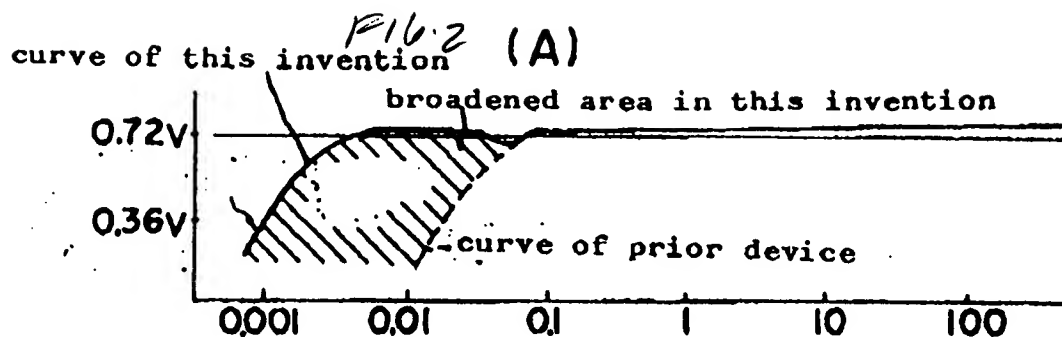


Fig 2 (A) object Lux (abscissa) and video output voltage (ordinate) of prior device (dotted line)

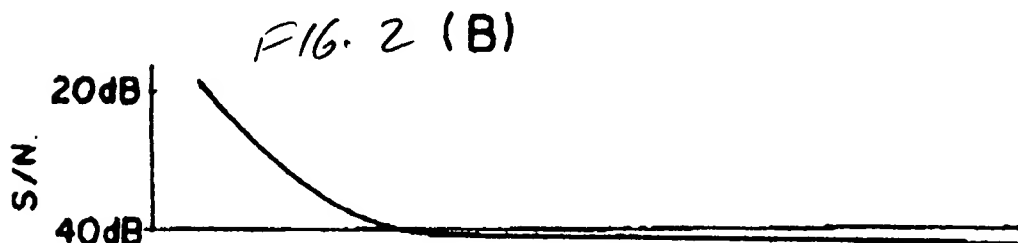


Fig 2 (B) object Lux (abscissa) and S/N ratio (ordinate) of this invention.

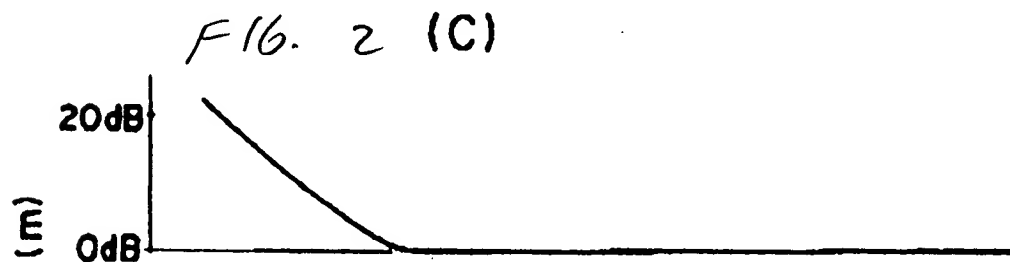


Fig 2(C) the relation of object Lux (abscissa) and gain of auxiliary amplifying circuit m (ordinate) of this invention.

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**Fig 3 ~~embodiment 2~~**

~~signal processing circuit~~

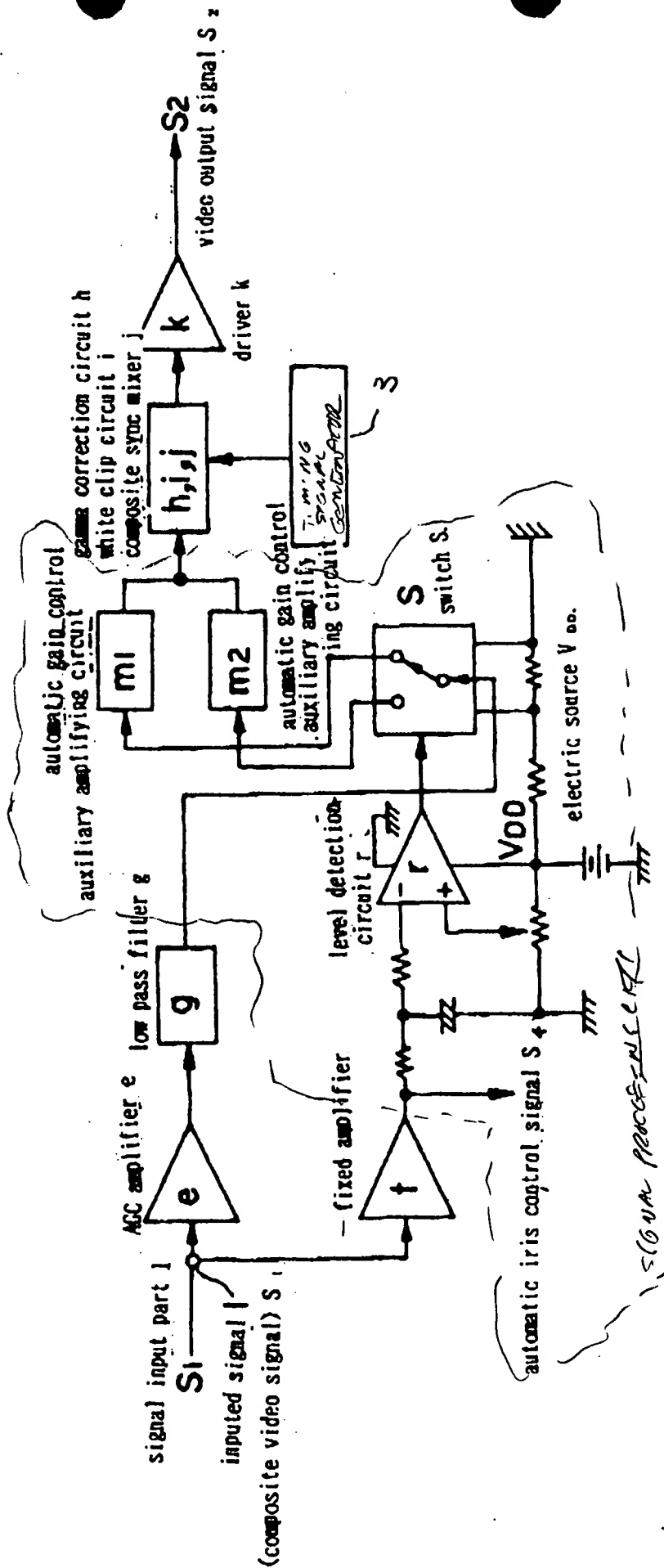


Fig 4. ~~embodiment 3~~

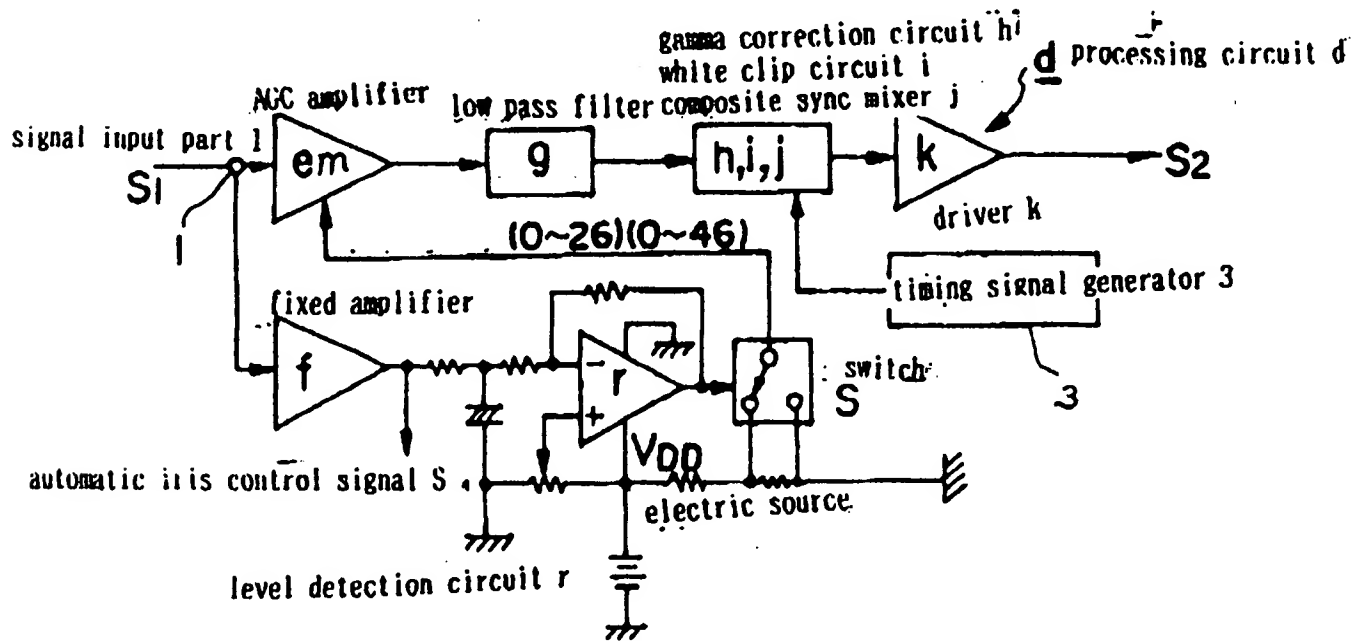
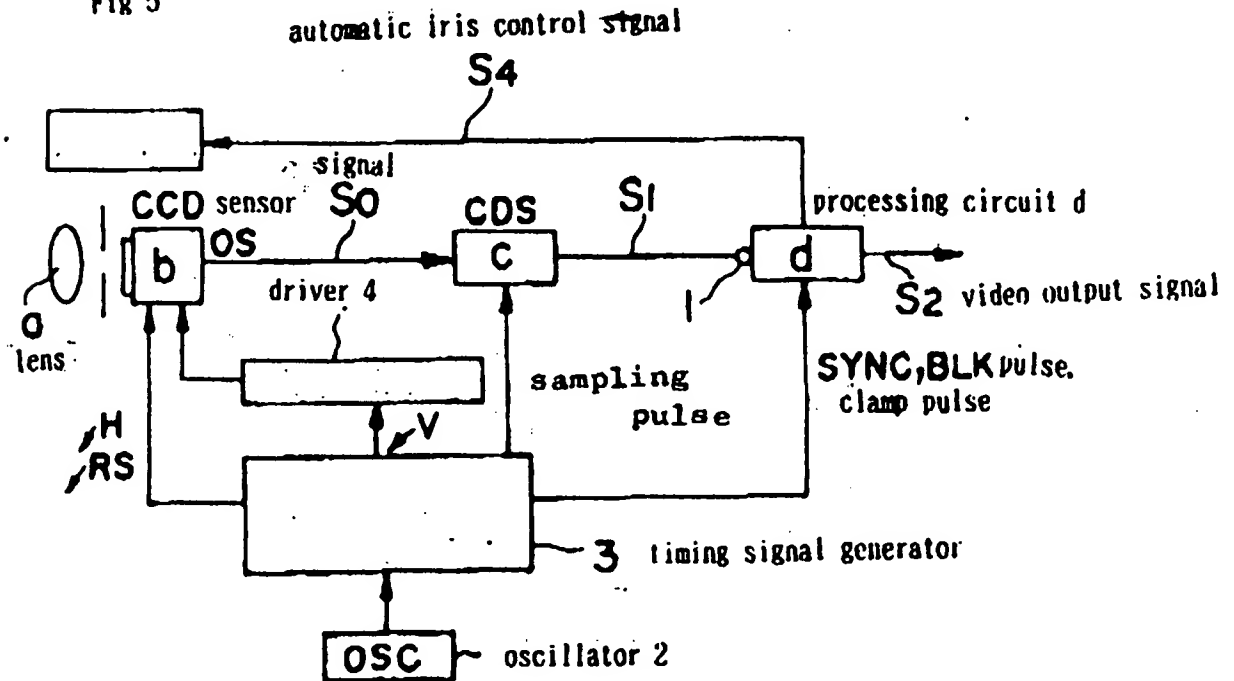


Fig 5



**Appendix A**

